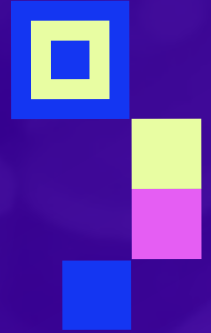




GLOBAL SEMICONDUCTOR CONFERENCE MALTA



AI and the Great Semiconductor Divide: Strengthening Europe's Competitiveness

16-17 November 2026

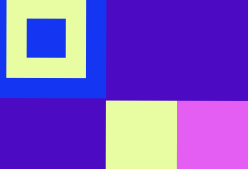
Westin Dragonara Resort



MALTAENTERPRISE



MALTA
SEMICONDUCTOR
COMPETENCE
CENTRE



DAY 1 – 16th November 2026

08:30 REGISTRATION

09:00 Welcome by **George Gregory**, CEO, Malta Enterprise & **Sean Redmond**, President, Silicon Catalyst EU

09:10 **Keynote Speech by Marc Hijink**, Author of FOCUS, The ASML Way: Lessons in Building a European Chip Champion

10:00 **Opening Address by Hon Silvio Schembri**, Minister for Economy, Technology & Strategic Projects

10:10 **Panel 1: From Lab to Fab: Leveraging Pilot Lines for Industrial Growth**

- **Giuseppe Fiorentino**, Programme Manager, imec IC-link, NanoIC, imec (Leading edge system on chip)
- **Dominique Noguet**, PhD, VP & FAMES Pilot Line Project Coordinator, CEA LETI
- **Dirk Schumann**, Director, APECS Project Manager, Research Fab Microelectronics, Germany
- **Sergi Ferrando**, Exploitation and Innovation Manager, PIXEurope
- **Francesco La Via**, Research Director, Coordinator WBG Pilot LINE

Moderator: Jalal Bagherli, Former CEO, Dialog Semiconductor

10:50 **Industry keynote speech by Thomas Morgenstern**, EVP, Manufacturing STMicroelectronics. **Building a Resilient, Highly Skilled, and Competitive European Semiconductor Capacity**

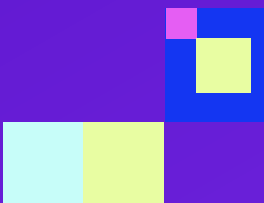
11:10 **Panel 2: Advancing Europe's Semiconductor Ecosystem: Lessons from IPCEI ME/CT and Outlook to IPCEI AST**

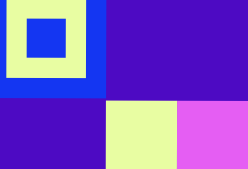
- **Roberto Zafalon**, Director, Europe Institutional Relationships – STMicroelectronics S.r.l.
- **Dr Sophie Cordeiro**, Public Funding, Consortia & Geopolitics – Robert Bosch GmbH
- **Cathy Batisson**, Lead, European Research & Innovation Partnerships and Public Funding – Renault Group
- **Martin Mischitz**, Director, Innovation & Funding Management – Infineon Technologies Austria AG
- **Tomáš Bort**, Director, Innovation & Public Affairs – Mycroft Mind

Moderator: Patrick Pype, Director, Strategic Partnerships & External Relations – NXP Semiconductors

12:00 COFFEE BREAK

12:15 **Keynote Speech by Mr Yankin Tanurhan**, CTO MIPS, GlobalFoundries

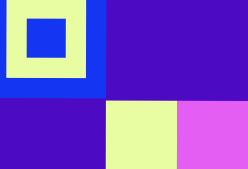




DAY 1 – 16th November 2026

12:45	Panel 3: Made in Europe & Strategic Autonomy – Would this Deepen the Divide or Strengthen the Union? • Matthew Xuereb, Policy Officer, Microelectronics & Photonics Unit, DG CNECT, European Commission • Tullio Ambrosone, Coordinator, IE University Competitiveness Hub Brussels; Director of the Arel Single Market Lab • Tillman Schenk, Research Analyst, Bruegel • Andrea Ganio, Chief Institutional Relations Officer of SPEA spa • Gordon Cordina, Executive Director, E-Cubed Consultants • Frederique Le Greves, President STMicroelectronics France and Executive Vice President Europe & France Public Affairs Moderator: Giovanni Tripodi, Strategic Advisor / Operations Lead, A&G Techminds
13:15	LUNCH
14:30	Panel 4: AI & the Value Chain Divide – Avoiding a Two-Speed Ecosystem and Ensuring Broad-Based Benefits • Patrick Bressler, Director for International Cooperation, Emerging Technologies and Innovation, Fraunhofer Microelectronics Group / FMD (Research-Fab Microelectronics Germany) • Christophe Frey, President ScaleU, Chairman of the board Vertical Computer • Arkadiusz Hruszowiec, General Manager, Commercialization & Strategy, CEZAMAT Warsaw University of Technology • Paolo Azzoni, Secretary General, INSIDE Industry Association Moderator: Nitin Dahad, Editor-in-Chief, EE Times
15:30	Panel 5: Women in Semiconductors - Driving Diversity, Innovation, and Growth • Dr Giorgia Longobardi, Founder & CMO, Cambridge GaN Devices Ltd • Jillian Hughes, European Operations Director, Silicon Catalyst • Selma Arici, Founder & CEO, SiRah Global Semiconductor Strategist • Géraldine Andrieux, CEO, BLUMORPHO • Antonietta Oliva, Founder, A&G TechMinds LLC/Founder – Women in Hardware / Former Apple Director • Karen Stinckens, VP Business Unit Position Sensors, Melexis Moderator: Romano Hoofman, Strategic Development Director, imec
16:15	Introduction to the Graduating ChipStart EU Cohort 1 Startups
17:30	END OF DAY 1
19:00	Departure from Westin Dragonara Resort
19:30	An Evening at Verdala Palace: Culture, Wine & Networking <i>An exclusive soirée celebrating Maltese culture, hospitality, fine local wines and cuisine, complemented by cultural entertainment and networking in the unique surroundings of one of Malta's most prestigious historic venues.</i>





DAY 2 – 17th November 2026

- 09:30** **Intro by Sean Redmond**
- 09:35** **Keynote Speech by Dr Giorgia Longobardi**, Founder, Cambridge GaN Devices Ltd
- 09:45** **Panel 1: Chips Competence Centres: From Centres to impact – Companies Perspectives**
- **Klaus Strohmayer**, CEO, Semify-eda (Austria)
 - **Thomas Galea**, Co-founder– Ice-Cap (Malta)
 - **Thomas Emil Le Cozannet**, Microfabrication / Space Engineer, Co-Founder, Spasic Aps (Denmark)
 - **Alessandro Brunetti**, Founder, iQrypto (Belgian)
- Moderator: John Goodenough**, Professor of Microelectronic Systems, Technology Leader in Research, Secure Microelectronic System Design and Automation
- 10:15** **Panel 2: More than a Cheque: Why the Right Investor is Your Best Component**
- **Charles P. Beyrouthy**, Forma Prime
 - **Samarth Vadia**, CEO and Co-Founder, Linque GmbH
 - **Miguel Borg**, CEO, Malta Venture Capital
 - **Martin MacDonald**, COO, weeteq
 - **Seamus Odriscoll**, CEO, Pulposemi
 - **Laura Swan**, General Partner, Silicon Catalyst Ventures
- Moderator: Stan Boland**, Investor & Advisor
- 11:00** **The Malta Experience: Fireside Chat with Sean Redmond President, Silicon Catalyst EU and Nitin Dahad , Editor-in-Chief, EE Times**
- 11:45** **Startup Pitches: ChipStart EU Cohort#2 Semiconductor Startup Pitches**
- 13:00** **LUNCH**
- 14:15** **Keynote Speech by Romano Hoofman**
The EU Chips Design Platform, a Catalyst for Fabless Design in Europe
- 14:30** **Panel 3: The importance of the EU Chips Design Platform for European Sovereignty and Economic Security**
- **Matthew Xuereb**, Policy Officer, Microelectronics & Photonics Unit, DG CNECT, European Commission
 - **Stephanie Dournelle**, Director EMEA Services, IP and Automotive SIEMENS EDA
 - **Damien Bretegnier**, DeepTech VC, Supernova Invest
 - **Vincent Casillas**, CTO, SiPearl
- Moderator: Giorgio Dell'Erba**, Director of Industrial Partnerships and Tech Transfer, Fondazione Chips-IT
- 15:00** **Pitching Sessions EuroCDP Accelerate**
- 16:30** **Pitching Sessions EuroCDP Incubate**
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